

05-02-07

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. of: Shawn D. ROGERS et al.Appln. No.: 10/802,664Examiner: Dickey, Thomas L.Filed: March 17, 2004Art Unit: 2826For: CIRCUIT AND METHOD FOR:  
ENHANCED LOW FREQUENCY  
SWITCHING NOISE SUPPRESSION IN  
MULTILAYER PRINTED CIRCUIT  
BOARDS USING A CHIP CAPACITOR  
LATTICEAttorney Docket No: 13050/12Mail Stop Amendment  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

## TRANSMITTAL

Sir:

## Attached are:

- ☒ Response to Non-Final Office Action dated Feb. 2, 2007 (16 pages).  
☒ Return Receipt Postcard.

## Fee calculation:

- ☐ No additional fee is required.  
☒ Small Entity.  
☐ An extension fee in an amount of \$\_\_\_\_\_ for a \_\_\_\_\_-month extension of time under 37 C.F.R. § 1.136(a).  
☐ A petition or processing fee in an amount of \$\_\_\_\_\_ under 37 C.F.R. § 1.17(\_\_\_\_).  
☐ An additional filing fee has been calculated as shown below:

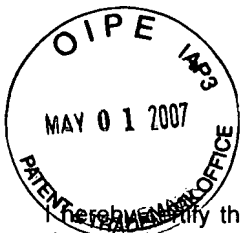
					Small Entity			Not a Small Entity	
	Claims Remaining After Amendment		Highest No. Previously Paid For	Present Extra	Rate	Add'l Fee	or	Rate	Add'l Fee
Total	71	Minus	71	0	x \$25=			x \$50=	
Indep.	7	Minus	7	0	X100=			x \$200=	
First Presentation of Multiple Dep. Claim					+ \$180=			+ \$360=	
					Total	\$		Total	\$

## Fee payment:

- ☐ A check in the amount of \$\_\_\_\_\_ is enclosed.  
☐ Please charge Deposit Account No. 23-1925 in the amount of \$\_\_\_\_\_. A copy of this Transmittal is enclosed for this purpose.  
☐ Payment by credit card in the amount of \$\_\_\_\_\_ (Form PTO-2038 is attached).  
☒ The Director is hereby authorized to charge payment of any additional filing fees required under 37 CFR § 1.16 and any patent application processing fees under 37 CFR § 1.17 associated with this paper (including any extension fee required to ensure that this paper is timely filed), or to credit any overpayment, to Deposit Account No. 23-1925.

Respectfully submitted,

Date 1.V.07Sid Bennett  
Sid Bennett (Reg. No. 53,981)BRINKS  
HOFFER  
GILSON  
& LIONEBRINKS HOFFER GILSON & LIONE  
NBC Tower - Suite 3600, 455 N. Cityfront Plaza Drive, Chicago, IL 60611-5599



I hereby certify that this correspondence is being deposited with the United States Postal Service, with sufficient postage, as Express Mail EV 963106909 US, in an envelope addressed to:

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
May 1, 2007

Date of Deposit

Sid Bennett (53,981)

Name of applicant, assignee or  
Registered Representative

Signature

Date of Signature

Our Case No. 13050/12

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Shawn D. ROGERS et al.

Serial No. 10/802,664

Filing Date: 03/17/2004

For: CIRCUIT AND METHOD FOR ENHANCED  
LOW FREQUENCY SWITCHING NOISE  
SUPPRESSION IN MULTILAYER  
PRINTED CIRCUIT BOARDS USING A  
CHIP CAPACITOR LATTICE

)  
)  
) Examiner: Dickey, Thomas L.  
)  
) Art Unit: 2826  
)  
) Confirmation No.: 2665  
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**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office action dated February 2, 2007. The Claims begin on page 2, and the Remarks begin on page 13.